Ref #	Hits	Search Query	DBs	Default Operator	Plurals
# L1	3618579	(semiconductor die chip ic (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L2	49061	(leadframe (lead adj frame)) same 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L3	8668	(paddle ((chip die) adj4 (pad paddle))) same 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L4	5031	wire same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L5	4979	lead and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON
L6	2729	(support supporting supportable supported) and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON
L7	2513	(package packaging packaged) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT IBM_TDB		ON
L8	2334	(encapsulating encapsulate encapsulant encase encapsulated resir epoxy) and 7	US-PGPUB USPAT; USOCR; EPO; JPO; DERWENT IBM_TDB		ON

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19 1976744 (substrate carrier board ((printed US-PGPUB; OR US-PGPUB) and "9" USOCR; EPO; JPO; DERWENT; BM_TDB (substrate carrier board ((printed US-PGPUB; OR Wiring cirucit) adj4 board)) and 8 USOCR; EPO; JPO; DERWENT; EPO; JPO; EPO; JPO; DERWENT; EPO; JPO; DERWENT; EPO; JPO; EPO; EPO; JPO; EPO; EPO; JPO; EPO; EPO; JPO; EPO; EPO; EPO; JPO; EPO; EPO; EPO; EPO; EPO; EPO; EPO; E	1976744 (substrate carrier board ((printed US-PGPUB; OR Wiring cirucit) adj4 board)) and "9" US-PGPUB; OR US-PGPUB; OR Wiring cirucit) adj4 board)) and 8 US-PGPUB; OR Wiring cirucit) adj4 board)) and 8 US-PGPUB; OR US-PGPUB; OR US-PGPUB; DERWEINT; IBM_TDB	7		₹ .		le	
1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB Wiring cirucit) adj4 board)) and 8 US-PAT; USPAT;	1976744 (substrate carrier board ((printed US-PGPUB), wiring cirucit) adj4 board)) and "g" USPAT;	O	O				
wiring cirucit) adj4 board)) and "9" 1948 (substrate carrier board ((printed wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8	L9 1976744 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" [10 1948 (substrate carrier board ((printed wiring cirucit) adj4 board))) and 8 [10 1948 (substrate carrier board ((printed wiring cirucit) adj4 board))] and 8 [10 1949 [10	Q	OR				
wiring cirucit) adj4 board)) and "9" (substrate carrier board ((printed wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8	L10 1948 (substrate carrier board ((printed wiring cirucit) adj4 board)) and "9" wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board)) and 8 wiring cirucit) adj4 board))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB				
1976744	L10 1948	(substrate carrier board ((printed wiring cirucit) adj4 board)) and "9"	(substrate carrier board ((printed wiring cirucit) adj4 board)) and 8				
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